

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

NEC

The 2SK3919 is N-channel MOS FET device that features a low on-state resistance and excellent switching characteristics, and designed for low voltage high current applications such as DC/DC converter with synchronous rectifier.

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3919	TO-251 (MP-3)
2SK3919-ZK	TO-252 (MP-3ZK)

FEATURES

- Low on-state resistance $R_{DS(on)1} = 5.6 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, \text{ ID} = 32 \text{ A})$
- Low Ciss: Ciss = 2050 pF TYP.
- 5 V drive available

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGs = 0 V)	VDSS	25	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	D(DC)	±64	А
Drain Current (pulse) Note1	D(pulse)	±256	А
Total Power Dissipation (Tc = 25°C)	P _{T1}	36	W
Total Power Dissipation	Pt2	1.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C
Single Avalanche Current Note2	las	27	А
Single Avalanche Energy Note2	Eas	73	mJ

(TO-251)



(TO-252)



Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Starting T_{ch} = 25°C, V_{DD} = 12.5 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V

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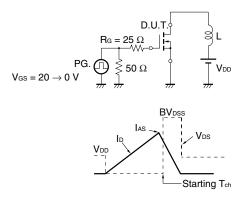
ELECTRICAL CHARACTERISTICS (T_A = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 25 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	2.0	2.5	3.0	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 16 A	9.7	19		S
Drain to Source On-state Resistance Note	RDS(on)1	Vgs = 10 V, Id = 32 A		4.5	5.6	mΩ
	RDS(on)2	Vgs = 5.0 V, I₂ = 16 A		6.8	13.7	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V		2050		pF
Output Capacitance	Coss	V _{GS} = 0 V		460		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		330		pF
Turn-on Delay Time	td(on)	VDD = 12.5 V, ID = 32 A		16		ns
Rise Time	tr	V _{GS} = 10 V		19		ns
Turn-off Delay Time	td(off)	R _G = 10 Ω		53		ns
Fall Time	tr			22		ns
Total Gate Charge	QG	V _{DD} = 20 V		42		nC
Gate to Source Charge	QGS	V _{GS} = 10 V		8		nC
Gate to Drain Charge	Qgd	ID = 64 A		15		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 64 A, VGS = 0 V		0.97		V
Reverse Recovery Time	trr	IF = 64 A, VGS = 0 V		23		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/µs		11		nC

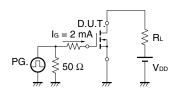
Note Pulsed

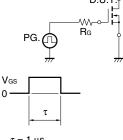
TEST CIRCUIT 1 AVALANCHE CAPABILITY

TEST CIRCUIT 2 SWITCHING TIME

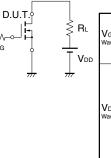


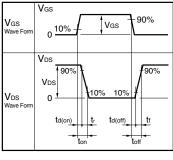
TEST CIRCUIT 3 GATE CHARGE



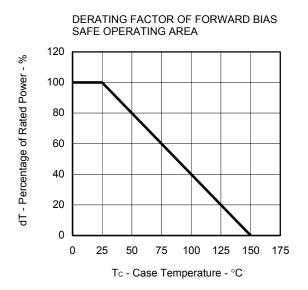


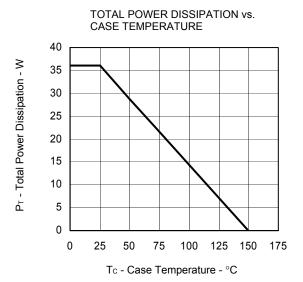
 $\begin{array}{l} \tau = 1 \; \mu s \\ \text{Duty Cycle} \leq 1\% \end{array}$



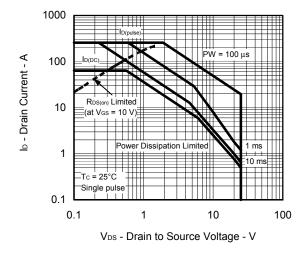


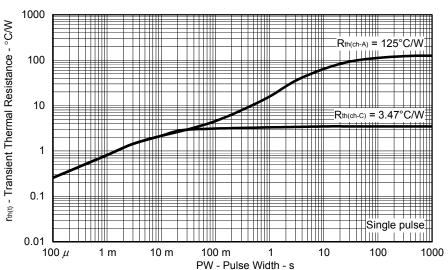
TYPICAL CHARACTERISTICS (T_A = 25°C)

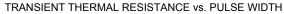


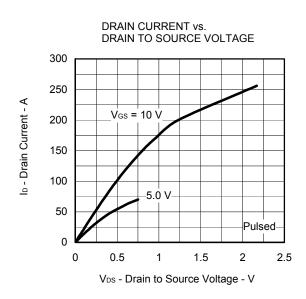


FORWARD BIAS SAFE OPERATING AREA

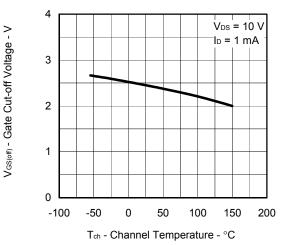


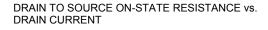


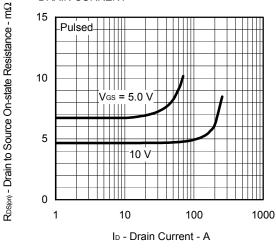




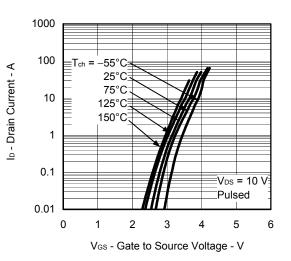




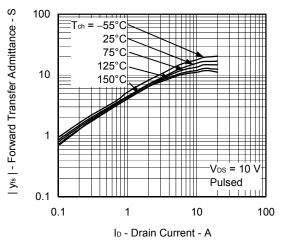




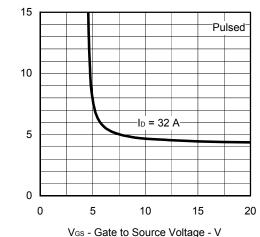
FORWARD TRANSFER CHARACTERISTICS



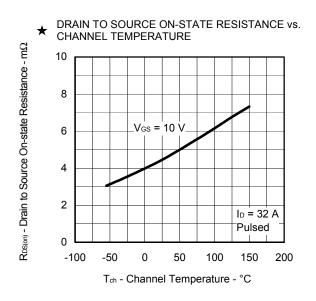
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



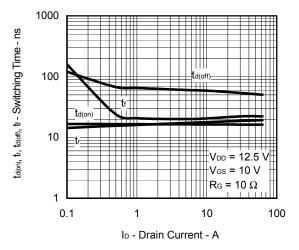
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



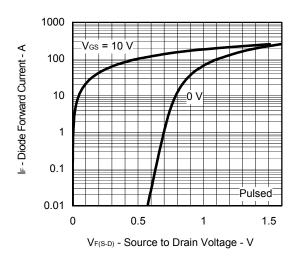
 $R_{\text{DS}(\text{on})}$ - Drain to Source On-state Resistance - $m\Omega$



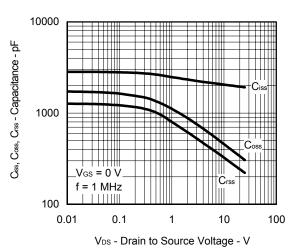
SWITCHING CHARACTERISTICS



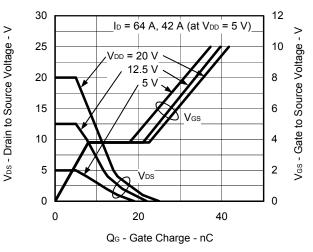
SOURCE TO DRAIN DIODE FORWARD VOLTAGE

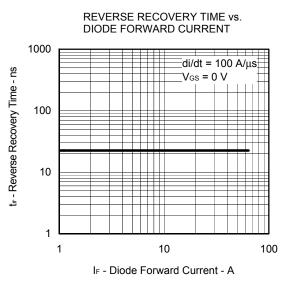


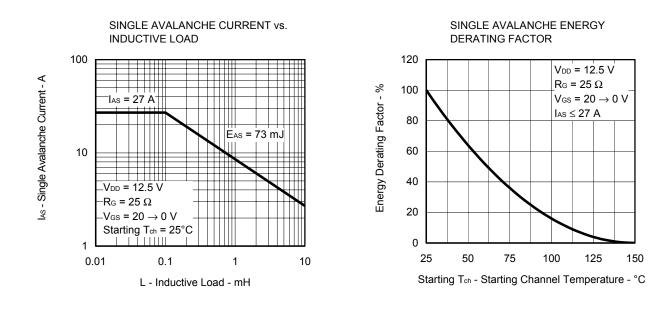




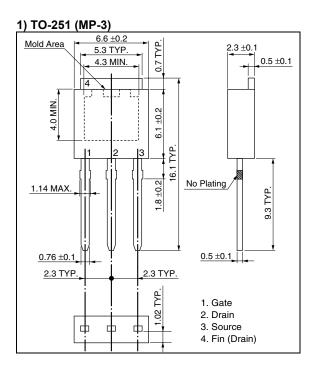
DYNAMIC INPUT/OUTPUT CHARACTERISTICS

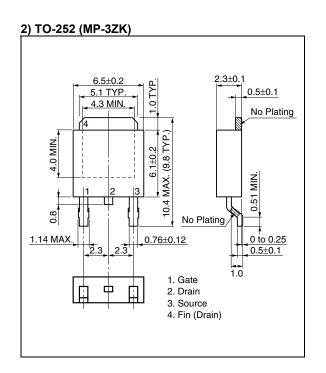




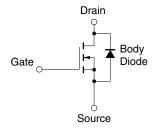


PACKAGE DRAWINGS (Unit: mm)





EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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