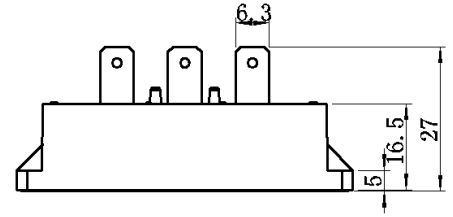
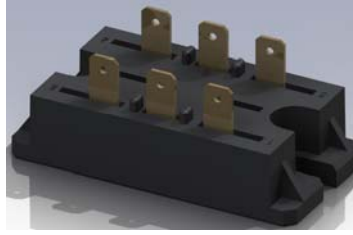


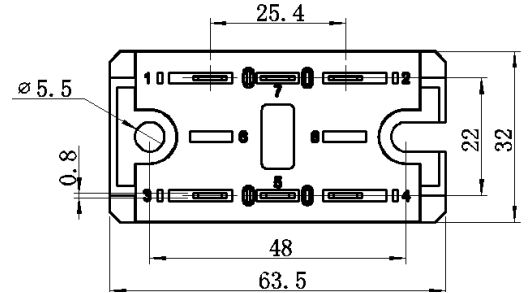
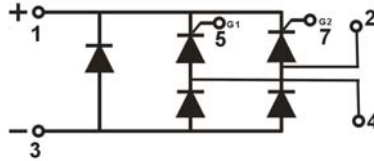
Feature

- International standard package
- Low forward voltage drop
- Isolation voltage 2500V~



Application

- Different kinds of rectifiers
- AC/DC electric machine control
- Heating control
- Dimming
- Inverters



Maximum value

| Symbol | Parameter | Rating | | unit |
|-----------|-------------------------------------|------------|------------|------|
| | | MFQ42.5-12 | MFQ42.5-16 | |
| V_{RRM} | Peak reverse repetitive voltage | 1200 | 1600 | V |
| V_{RSM} | Peak reverse non-repetitive voltage | 1300 | 1700 | V |
| V_{DRM} | Peak off-state repetitive voltage | 1200 | 1600 | V |

| Symbol | Parameter | Test condition | Rating | Unit |
|--------------------------|-------------------------------------|--|-------------|------------|
| I_O | Average on-state current | Single-phase full-wave rectifying circuit $T_C:85^\circ C$ | 42.5 | A |
| $I_{T(RMS)}, I_{F(RMS)}$ | Forward RMS current | Single-side heat-dissipation, $180^\circ \sin$ half wave, 50Hz, $T_C:85^\circ C$ | 67 | A |
| I_{TSM}, I_{FSM} | Forward surge current | $t=10ms, 50Hz, \sin, T_j$ | 460 | A |
| I^2t | I^2t value | $V_R = 0.6V_{RRM}, T_j$ | 1050 | A^2S |
| P_{GM} | Peak gate power | | 10 | W |
| $P_{G(AV)}$ | Average gate power | | 0.5 | W |
| di/dt | On-state current critical rise rate | $I_{GM}=1.5A, t_r \leq 0.5\mu s, T_j=25^\circ C$ | 100 | $A/\mu s$ |
| V_{ISO} | Isolation voltage | AC one minute | 2500 | V |
| T_j | Operating junction temperature | | -40 to +125 | $^\circ C$ |
| T_{jm} | Rated junction temperature | | 125 | $^\circ C$ |
| T_{stg} | Storage temperature | | -40 to +125 | $^\circ C$ |
| M_d | Mounting torque (copper plate) M6 | | 4 | N·m |
| W_t | Weight | | 76 | g |

Electrical characteristics

| Symbol | Parameter | Test condition | Rating | Unit |
|-------------------|-------------------------------------|--|---------|-------------|
| I_{DRM} | Peak off-state repetitive current | One-side heat-dissipation, $V_D=V_{DRM}$, sine half wave, $T_j=125^\circ C$ | 8 | mA |
| I_{RRM} | Peak reverse repetitive current | One-side heat-dissipation, $V_R=V_{RRM}$, sine half wave, $T_j=125^\circ C$ | 8 | mA |
| V_{TM} / V_{FM} | Peak forward voltage | $I_{TM} / I_{FM}=60A, T_j=25^\circ C$ | 1.6/1.3 | V |
| V_{GT} | Gate trigger voltage | $T_j=25^\circ C, I_T=1A, V_D=12V$ | 0.7-1.5 | V |
| I_{GT} | Gate trigger current | $T_j=25^\circ C, I_T=1A, V_D=12V$ | 20-100 | mA |
| V_{GD} | Gate non-trigger voltage | $T_j=125^\circ C, V_D=2/3V_{DRM}$ | 0.2 | V |
| I_{GD} | Gate non-trigger current | $T_j=125^\circ C, V_D=2/3V_{DRM}$ | 10 | mA |
| dv/dt | On-state voltage critical rise rate | $T_j=125^\circ C, V_D=2/3V_{DRM}$ | 500 | $V/\mu s$ |
| I_H | Holding current | $T_j=25^\circ C$ | 20-100 | mA |
| I_L | Latching current | $T_j=25^\circ C$ | 100-400 | mA |
| $R_{th(j-c)}$ | Thermal impedance (junction-case) | One-side heat dissipation, sine half wave | 0.24 | $^\circ CW$ |