

## **Power line chokes**

Current-compensated ring core double chokes 250 V AC, 0.3 ... 3 A, 1.2 ... 68 mH

Series/Type: B82722A/J

Date: July 2012



Power line chokes B82722A/J

## Current-compensated ring core double chokes

Rated voltage 250 V AC Rated current 0.3 ... 3 A Rated inductance 1.2 ... 68 mH

#### Construction

- Current-compensated ring core double choke
- Ferrite core wih epoxy coating (UL 94 V-0)
- Polycarbonate case (UL 94 V-0)
- Polyurethane potting (UL 94 V-0)
- Sector winding

#### **Features**

- High resonance frequency due to special winding technique
- Approx. 1% stray inductance for symmetrical interference suppression
- Suitable for wave soldering
- Design complies with EN 60938-2 (VDE 0565-2) and UL 1283
- RoHS-compatible

## **Applications**

- Suppression of common-mode interferences
- Switch-mode power applications
- Electronic ballasts in lamps
- Power inverters

#### **Terminals**

- Base material CuNi18Zn20
- Layer composition Ni, Sn
- Hot-dipped
- $\blacksquare$  Pins  $0.7 \times 0.7$  (mm)
- Lead spacing 10 × 12.5 (mm) or 20 × 12.5 (mm)

#### Marking

Manufacturer, approval signs and/or VDE standard number, ordering code, graphic symbol, rated current, rated voltage, rated inductance, date of manufacture (YYWWD.internal ID code)

## **Delivery mode**

Blister tray in cardboard box



B82722A



B82722J

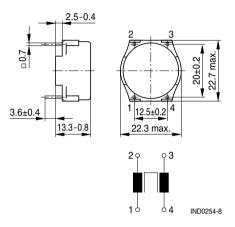
<sup>1)</sup> UL approval with 300 V AC



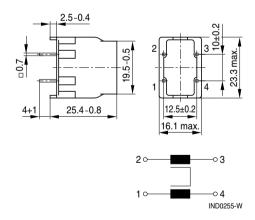
## Current-compensated ring core double chokes

## Dimensional drawings and pin configurations

Horizontal version (B82722A)



## Vertical version (B82722J)



Tolerances to ISO 2768-C unless otherwise noted. Dimensions in mm.





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## **Current-compensated ring core double chokes**

## Technical data and measuring conditions

Rated voltage V <sub>R</sub>	250 V AC (50/60 Hz)		
Test voltage V <sub>test</sub>	1500 V AC, 2 s (line/line)		
Rated temperature T <sub>R</sub>	+40 °C or +60 °C		
Rated current I <sub>R</sub>	Referred to 50 Hz and rated temperature		
Rated inductance L <sub>R</sub>	Measured with Agilent 4284A at 10 kHz, 0.1 mA, +20 °C Inductance is specified per winding.		
Inductance tolerance	±30% at +20 °C		
Inductance decrease ΔL/L <sub>0</sub>	< 10% at DC magnetic bias with I <sub>R</sub> , +20 °C		
Stray inductance L <sub>stray,typ</sub>	Measured with Agilent 4284A at 10 kHz, 5 mA, +20 °C, typical values		
DC resistance R <sub>typ</sub>	Measured at +20 °C, typical values, specified per winding		
Solderability (lead-free)	Sn96.5Ag3.0Cu0.5: +(245 $\pm$ 5) °C, (3 $\pm$ 0.3) s Wetting of soldering area $\geq$ 95% (to IEC 60068-2-20, test Ta)		
Resistance to soldering heat (wave soldering)	+(260 ±5) °C, (10 ±1) s (to IEC 60068-2-20, test Tb)		
Climatic category	40/125/56 (to IEC 60068-1)		
Storage conditions (packaged)	–25 °C +40 °C, ≤ 75% RH		
Weight	Approx. 10 g		
Approvals	EN 60938-2, UL 1283		



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## **Current-compensated ring core double chokes**

## Characteristics and ordering codes

I <sub>R</sub>	L <sub>R</sub>	L <sub>stray,typ</sub>	$R_{typ}$	$T_{R}$	Ordering code		Approvals	
Α	mH	μН	mΩ	°C	Horizontal version	Vertical version	<u> </u>	<i>7</i> /
0.3	68	800	2500	+60	B82722A2301N002	B82722J2301N002	×	×
0.3	47	700	2500	+60	B82722A2301N001	B82722J2301N001	×	×
0.5	56	600	2000	+40	B82722A2501N020	B82722J2501N020	×	×
0.5	47	550	1500	+60	B82722A2501N022	B82722J2501N022	×	×
0.5	39	400	1120	+60	B82722A2501N021	B82722J2501N021	×	×
0.5	27	350	1200	+60	B82722A2501N001	B82722J2501N001	×	×
8.0	27	270	600	+60	B82722A2801N020	B82722J2801N020	×	×
1	15	170	540	+60	B82722A2102N020	B82722J2102N020	×	×
1	10	150	480	+60	B82722A2102N001	B82722J2102N001	×	×
1.5	10	90	240	+60	B82722A2152N020	B82722J2152N020	×	×
1.3	6.8	90	230	+60	B82722A2132N001	B82722J2132N001	×	×
1.7	4	45	175	+60	B82722A2172N001	B82722J2172N001	_	×
2	4.2	45	130	+40	B82722A2202N020	B82722J2202N020	×	×
2	3.3	35	133	+60	B82722A2202N002	B82722J2202N002	_	_
2	2.2	30	130	+60	B82722A2202N001	B82722J2202N001	×	×
2.5	1.7	20	80	+60	B82722A2252N001	B82722J2252N001	×	×
3	1.2	17	56	+60	B82722A2302N001	B82722J2302N001	×	×

x = approval granted

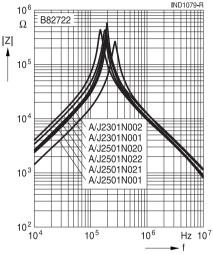




## Current-compensated ring core double chokes

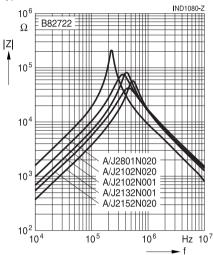
### Impedance |Z| versus frequency f

measured with windings in parallel at +20 °C, typical values



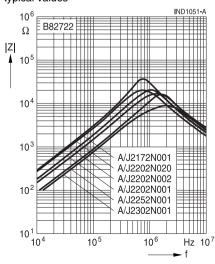
## Impedance |Z| versus frequency f

measured with windings in parallel at +20  $^{\circ}$ C, typical values

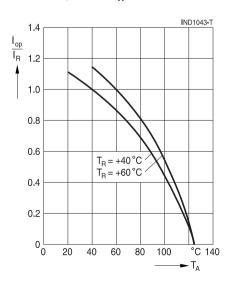


## Impedance |Z| versus frequency f

measured with windings in parallel at +20 °C, typical values



# Current derating $I_{op}/I_R$ versus temperature $T_{\Delta}$





## Cautions and warnings

### Current-compensated ring core double chokes

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
  - Particular attention should be paid to the derating curves given there. Derating must be applied
    in case the ambient temperature in the application exceeds the rated temperature of the
    component.
  - Ensure the operation temperature (which is the sum of the ambient temperature and the temperature rise caused by losses / self-heating) of the component in the application does not exceed the maximum value specified in the climatic category.
  - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.
- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation.
  - Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.
- The following points must be observed if the components are potted in customer applications:
  - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
  - It is necessary to check whether the potting material used attacks or destroys the wire insulation, plastics or glue.
  - The effect of the potting material can change the high-frequency behaviour of the components.
- Ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.



### Important notes

## Current-compensated ring core double chokes

The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
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