Dual Channel Small Outline Optoisolators

Transistor Output (Low Input Current)

The MOCD217 device consists of two gallium arsenide infrared emitting diodes optically coupled to two monolithic silicon phototransistor detectors, in a surface mountable, small outline, plastic package. It is ideally suited for high density applications and eliminates the need for through–the–board mounting.

- · Dual Channel Coupler
- Convenient Plastic SOIC-8 Surface Mountable Package Style
- Low Input Current (Specified @ 1 mA)
- Minimum V_{(BR)CEO} of 30 Volts Guaranteed
- Standard SOIC–8 Footprint, with 0.050" Lead Spacing
- · Shipped in Tape and Reel, which conforms to EIA Standard RS481A
- · Compatible with Dual Wave, Vapor Phase and IR Reflow Soldering
- High Input-Output Isolation of 3000 Vac (rms) Guaranteed
- Meets U.L. Regulatory Requirements, File #E54915

Ordering Information:

- To obtain MOCD217 in tape and reel, add R2 suffix to device number as follows:
 R2 = 2500 units on 13" reel
- To obtain MOCD217 in quantities of 50 (shipped in sleeves) no suffix

Marking Information:

MOCD217 = D217

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit					
INPUT LED								
Forward Current — Continuous	lF	60	mA					
Forward Current — Peak (PW = 100 μs, 120 pps)	I _F (pk)	1.0	А					
Reverse Voltage	V _R	6.0	V					
LED Power Dissipation @ T _A = 25°C Derate above 25°C	PD	90 0.8	mW mW/°C					
OUTPUT TRANSISTOR								
Collector–Emitter Voltage	VCEO	30	V					
Collector-Base Voltage	VCBO	70	V					
Emitter-Collector Voltage	VECO	7.0	V					
Collector Current — Continuous	IC	150	mA					
Detector Power Dissipation @ T _A = 25°C	PD	150	mW					

1.76

mW/°C

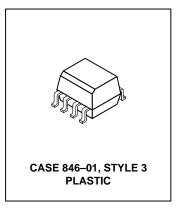
NOTE: Thickness through insulation between input and output is ≥ 0.5 mm.

MOCD217

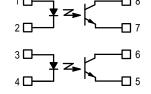
[CTR = 100% Min]

Motorola Preferred Device

DUAL CHANNEL SMALL OUTLINE OPTOISOLATOR TRANSISTOR OUTPUT







- 1. ANODE 1
- 2. CATHODE 1
- 3. ANODE 2
- 4. CATHODE 2
- 5. EMITTER 2
- 6. COLLECTOR 2
- 7. EMITTER 1
- 8. COLLECTOR 1

o. COLLECTOR I

Preferred devices are Motorola recommended choices for future use and best overall value.

Derate above 25°C

MOCD217

TOTAL DEVICE

MAXIMUM RATINGS — continued ($T_A = 25^{\circ}C$ unless otherwise noted)

Rating

Input–Output Isolation Voltage ^(1,2) (60 Hz, 1.0 sec. duration)		Viso		3000		Vac(rms)	
Total Device Power Dissipation @ T _A = 25°C Derate above 25°C			PD	250 2.94		mW mW/°C	
Ambient Operating Temperature Range ⁽³⁾			T _A	-55 to +100		°C	
Storage Temperature Range ⁽³⁾			T _{stg}	-55 to +150		°C	
Lead Soldering Temperature (1/16" from case, 10 sec. duration)			_	260		°C	
ELECTRICAL CHARACTERIST	ICS (T _A = 25°C unless otherwise	e noted) ⁽⁴⁾				1	
Character	ristic	Symbol	Min	Typ ⁽⁴⁾	Max	Unit	
INPUT LED							
Forward Voltage (I _F = 1.0 mA)		٧F	_	1.05	1.3	V	
Reverse Leakage Current (V _R = 6.0 V)		IR		0.1	100	μΑ	
Capacitance		С		18	_	pF	
OUTPUT TRANSISTOR			•	•		•	
Collector–Emitter Dark Current	$(V_{CE} = 5.0 \text{ V}, T_{A} = 25^{\circ}\text{C})$	ICEO1		1.0	50	nA	
	$(V_{CE} = 5.0 \text{ V}, T_{A} = 100^{\circ}\text{C})$	ICEO2		1.0	_	μΑ	
Collector–Emitter Breakdown Voltage (I _C = 100 μA)		V(BR)CEC	30	90	_	V	
Emitter–Collector Breakdown Voltage (I _E = 100 μA)		V(BR)ECC	7.0	7.8	_	V	
Collector–Emitter Capacitance (f = 1.0 MHz, V _{CE} = 0)		CCE	_	7.0	_	pF	
COUPLED			•			•	
Output Collector Current (I _F = 1.0 mA, V _{CE} = 5.0 V)	MOCD217	I _C (CTR) ⁽⁵	1.0 (100)	1.3 (130)	_	mA (%)	
Collector–Emitter Saturation Voltage	e ($I_C = 100 \mu\text{A}, I_F = 1.0 \text{mA}$)	VCE(sat)		0.35	0.4	V	
Turn–On Time (I _C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω)		ton	_	7.5	_	μs	
Turn–Off Time (I _C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω)		toff	_	5.7	_	μs	
Rise Time (I _C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω)		t _r	_	3.2	_	μs	
Fall Time (I _C = 2.0 mA, V _{CC} = 10 V, R _L = 100 Ω)		tf	_	4.7	_	μs	
Input-Output Isolation Voltage (f = 60 Hz, t = 1.0 sec.)(1,2)		VISO	3000	_	_	Vac(rms)	
Isolation Resistance (V _{I-O} = 500 V)(2)	RISO	10 ¹¹	_	_	Ω	

CISO

Symbol

Value

Unit

- 1. Input-Output Isolation Voltage, $V_{\mbox{ISO}}$, is an internal device dielectric breakdown rating.
- 2. For this test, pins 1, 2, 3 and 4 are common, and pins 5, 6, 7 and 8 are common.
- 3. Refer to Quality and Reliability Section in Opto Data Book for information on test conditions.
- 4. Always design to the specified minimum/maximum electrical limits (where applicable).
- 5. Current Transfer Ratio (CTR) = I_C/I_F x 100%.

Isolation Capacitance $(V_{I-O} = 0, f = 1.0 \text{ MHz})(2)$

0.2

pF

TYPICAL CHARACTERISTICS

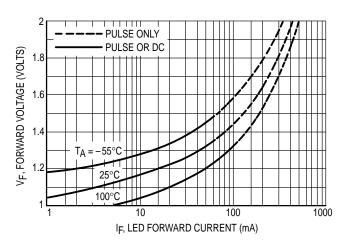


Figure 1. LED Forward Voltage versus Forward Current

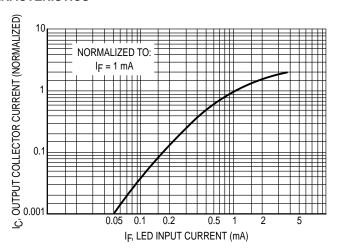


Figure 2. Output Current versus Input Current

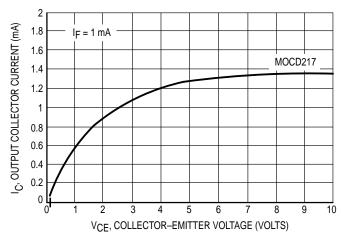


Figure 3. Output Current versus Collector–Emitter Voltage

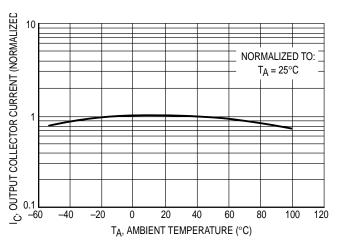


Figure 4. Output Current versus Ambient Temperature

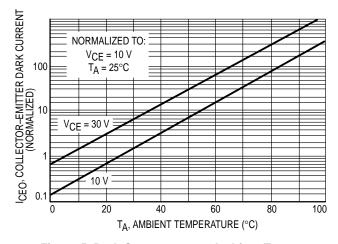


Figure 5. Dark Current versus Ambient Temperature

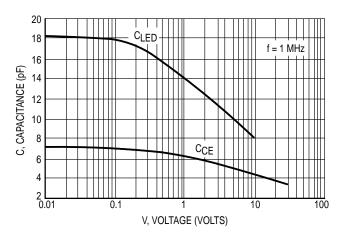
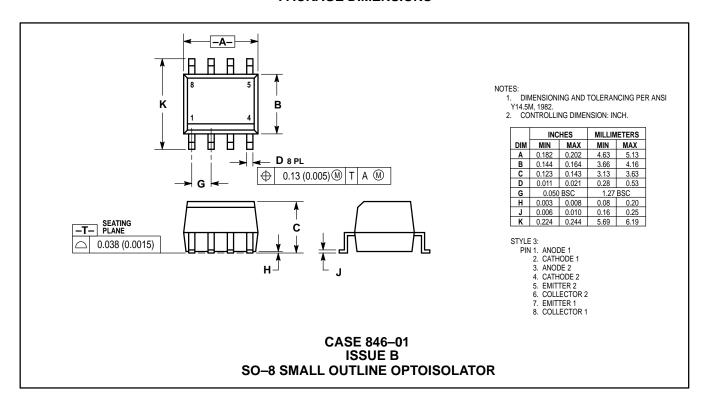


Figure 6. Capacitance versus Voltage

PACKAGE DIMENSIONS



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