

PC814XJ0000F Series

*4-channel package type is also available. (model No. **PC844XJ0000F Series**)

DIP 4pin AC Input Photocoupler



■ Description

PC814XJ0000F Series contains an IRED optically coupled to a phototransistor.

It is packaged in a 4pin DIP, available in SMT gullwing lead-form option.

Input-output isolation voltage(rms) is 5.0kV.

Collector-emitter voltage is 80V and CTR is 20% to 300% at input current of ±1mA.

■ Features

- 1. 4pin DIP package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. AC input type
- 4. High collector-emitter voltage (V_{CEO}: 80V)
- 5. Current transfer ratio (CTR : MIN. 20% at $I_{F=\pm}1mA$, $V_{CE=5}V$)
- 6. High isolation voltage between input and output $(V_{iso(rms)}: 5.0 \text{ kV})$
- 7. Lead-free and RoHS directive compliant

■ Agency approvals/Compliance

- 1. Recognized by UL1577, file No. E64380 (as model No. **PC814**)
- 2. Approved by VDE (DIN EN60747-5-2^(*)) (as an option), file No. 40008087 (as model No. **PC814**)
- 3. Package resin: UL flammability grade (94V-0)

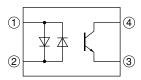
■ Applications

- 1. Programmable controllers
- 2. Telephone sets, telephone exchangers
- 3. System appliances
- 4. Signal transmission between circuits of different potentials and impedances

^(*) DIN EN60747-5-2: successor standard of DIN VDE0884



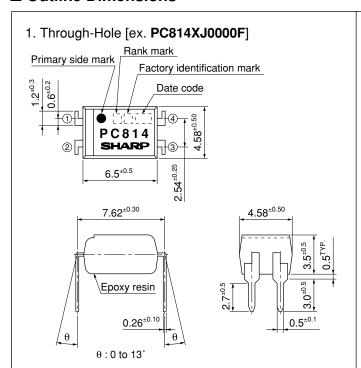
■ Internal Connection Diagram

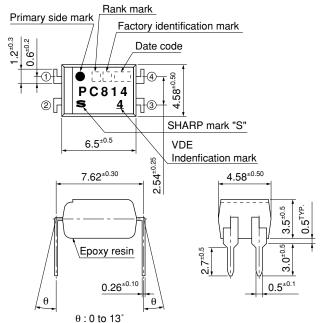


- 1 Anode/Cathode
- ② Cathode/Anode
- 3 Emitter
- 4 Collector

■ Outline Dimensions

(Unit: mm)





Product mass: approx. 0.23g

2. Through-Hole (VDE option) [ex. PC814XNYJ00F]

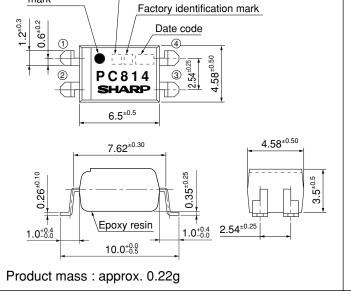
Product mass: approx. 0.23g

mark

3. SMT Gullwing Lead-Form [ex. **PC814XPJ000F**]

Primary side

Rank mark



Plating material: SnCu (Cu: TYP. 2%)



Date code (2 digit)

	1st o	digit		2nd digit		
	Year of p	roduction		Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Factory identification mark

Factory identification Mark	Country of origin	
no mark	Tomon	
	Japan	
	Indonesia	
$\overline{\hspace{1cm}}$	Philippines	
_	China	

^{*} This factory marking is for identification purpose only.
Please contact the local SHARP sales representative to see
the actual status of the production.

Rank mark

Refer to the Model Line-up table



■ Absolute Maximum Ratings

	■ Absolute Maximum Ratings $(T_a=25^{\circ}C)$							
	Parameter	Symbol	Rating	Unit				
	Forward current	I_F	±50	mA				
Input	*1 Peak forward current	I_{FM}	±1	A				
<u> </u>	Power dissipation	P	70	mW				
	Collector-emitter voltage	V_{CEO}	80	V				
Output	Emitter-collector voltage	V _{ECO}	6	V				
Out	Collector current	I_{C}	50	mA				
	Collector power dissipation	P_{C}	150	mW				
-	Total power dissipation	P _{tot}	200	mW				
*2]	solation voltage	V _{iso (rms)}	5.0	kV				
(Operating temperature	T_{opr}	-30 to +100	°C				
	Storage temperature	T _{stg}	-55 to +125	°C				
*3 (Soldering temperature	T_{sol}	260	°C				

^{*1} Pulse width≤100μs, Duty ratio : 0.001 *2 40 to 60%RH, AC for 1 minute, f=60Hz *3 For 10s

■ Electro-optical Characteristics

 $(T_a=25^{\circ}C)$

	Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
	Forward voltage		V_F	I _F =±20mA	_	1.2	1.4	V
Input	Peak forward voltage		V_{FM}	$I_{FM}=\pm0.5A$	_	_	3.0	V
	Terminal capacitance		C_{t}	V=0, f=1kHz	_	30	250	pF
	Collector dark current		I_{CEO}	$V_{CE} = 50V, I_{F} = 0$	_	-	100	nA
Output	Collector-emitter breakdown voltage		$\mathrm{BV}_{\mathrm{CEO}}$	$I_{C}=0.1 \text{ mA}, I_{F}=0$	80	_	_	V
	Emitter-collector breakdown voltage		BV_{ECO}	$I_{E}=10\mu A, I_{F}=0$	6	-	-	V
	Collector current		I_{C}	$I_F=\pm 1$ mA, $V_{CE}=5$ V	0.2	_	3.0	mA
	Collector-emitter saturation voltage		V _{CE (sat)}	$I_F=\pm 20mA$, $I_C=1mA$	_	0.1	0.2	V
Transfer	Isolation resistance		R _{ISO}	DC500V, 40 to 60%RH	5×10 ¹⁰	1×10 ¹¹	_	Ω
	Floating capacitance		C_{f}	V=0, f=1MHz	_	0.6	1.0	pF
	Cutt-off frequency		f_C	V_{CE} =5 V , I_{C} =2 mA , R_{L} =100 Ω , -3 dB	15	80	_	kHz
		Rise time	t_r	V 0V I 2 A B 1000	_	4	18	μs
	Response time	Fall time	t_{f}	V_{CE} =2V, I_{C} =2mA, R_{L} =100 Ω	_	3	18	μs



■ Model Line-up

Lead Form	Throug	gh-Hole	SMT Gullwing		I _C [mA]	
Package	Sle	eve	Taping	D 1 1		
	100pcs/sleeve		2 000pcs/reel	Rank mark	$(I_F=\pm 1 \text{mA}, V_{CE}=5 \text{V}, T_a=25 ^{\circ}\text{C})$	
DIN EN60747-5-2	Approved					
Model No.	PC814XJ0000F	PC814XNYJ00F	PC814XPJ000F	with or without	0.2 to 3.0	
	PC814X1J000F	PC814X1YJ00F	PC814XP1J00F	A	0.5 to 1.5	

Please contact a local SHARP sales representative to inquire about production status.



Fig.1 Forward Current vs. Ambient Temperature

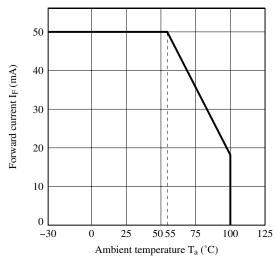


Fig.3 Collector Power Dissipation vs. Ambient Temperature

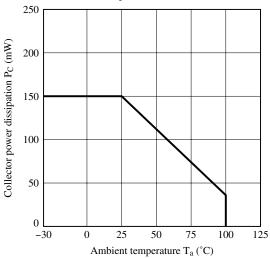


Fig.5 Peak Forward Current vs. Duty Ratio

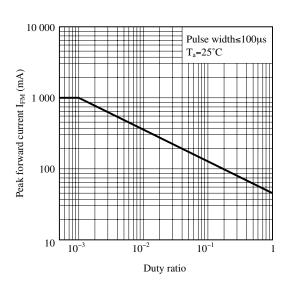


Fig.2 Diode Power Dissipation vs.
Ambient Temperature

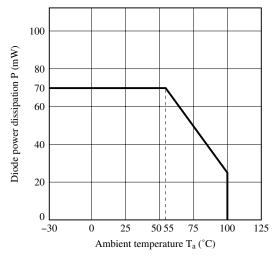


Fig.4 Total Power Dissipation vs. Ambient Temperature

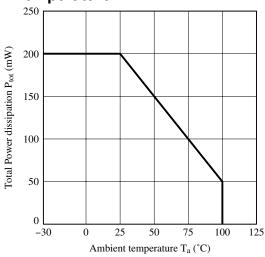


Fig.6 Forward Current vs. Forward Voltage

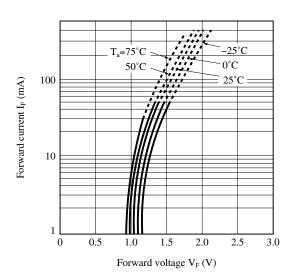




Fig.7 Current Transfer Ratio vs. Forward Current

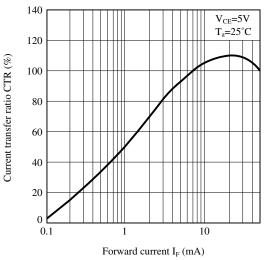


Fig.9 Relative Current Transfer Ratio vs.
Ambient Temperature

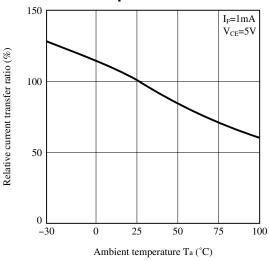


Fig.11 Collector Dark Current vs. Ambient Temperature

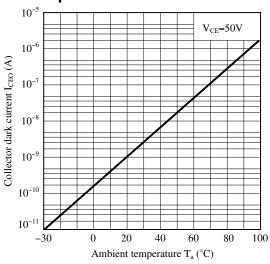


Fig.8 Collector Current vs. Collector-emitter Voltage

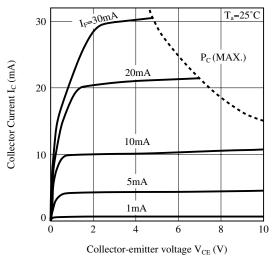


Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature

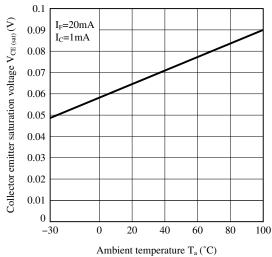
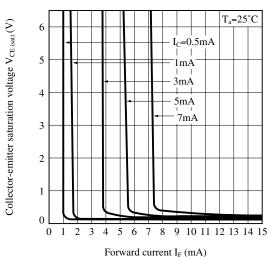


Fig.12 Collector-emitter Saturation Voltage vs. Forward Current



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Fig.13 Response Time vs. Load Resistance

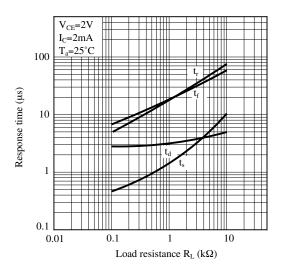


Fig.15 Frequency Response

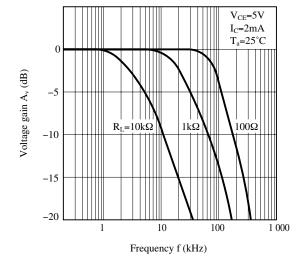
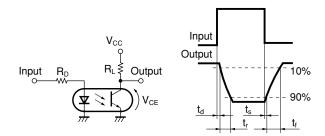
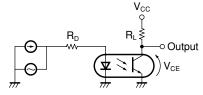


Fig.14 Test Circuit for Response Time



Please refer to the conditions in Fig.13.

Fig.16 Test Circuit for Frequency Response



Please refer to the conditions in Fig.15.

Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Design guide

While operating at I_F<1.0mA, CTR variation may increase.

Please make design considering this fact.

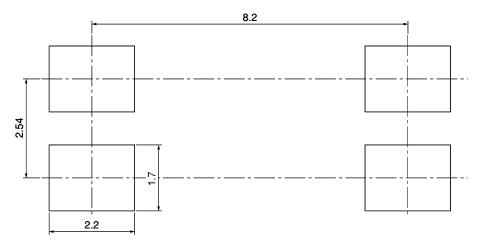
This product is not designed against irradiation and incorporates non-coherent IRED.

Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5years) into the design consideration.

Recommended Foot Print (reference)



(Unit:mm)

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

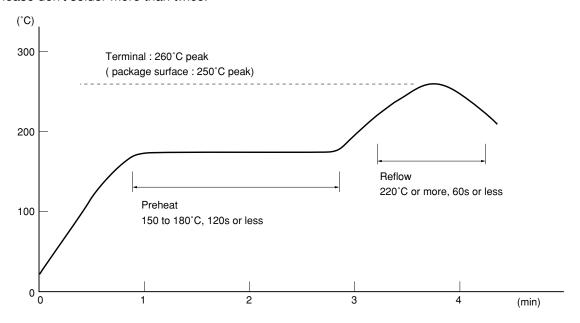
Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBB and PBDE are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive.

•Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).

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■ Package specification

Sleeve package

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

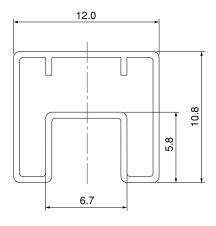
Package method

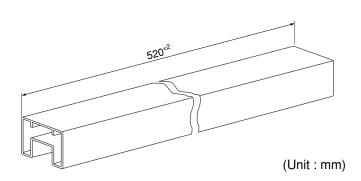
MAX. 100pcs of products shall be packaged in a sleeve. Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions







● Tape and Reel package

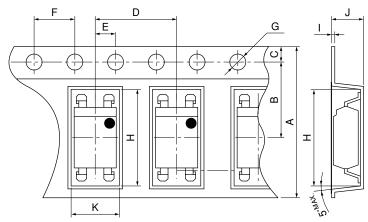
Package materials

Carrier tape: PS

Cover tape: PET (three layer system)

Reel: PS

Carrier tape structure and Dimensions

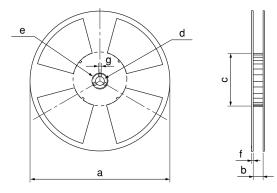


Dimensions List

(Unit: mm)

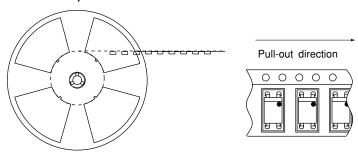
A	В	С	D	Е	F	G
16.0 ^{±0.3}	7.5 ^{±0.1}	1.75 ^{±0.10}	8.0 ^{±0.1}	2.0 ^{±0.1}	4.0 ^{±0.1}	φ1.5 + 8:δ
Н	I	J	K			
10.4 ^{±0.1}	0.40 ^{±0.05}	4.2 ^{±0.1}	5.1 ^{±0.1}			

Reel structure and Dimensions



Dir	mensio	ns List	(Unit: mm)		
	a b		с	d	
	ф330 17.5 ^{±1.5}		ф100.0 ^{±1.0}	ф13.0 ^{±0.5}	
	e	f	g		
φ2	23.0±1.0	2.0±0.5	2.0 ^{±0.5}		

Direction of product insertion



[Packing: 2 000pcs/reel]



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